

FIG. 1

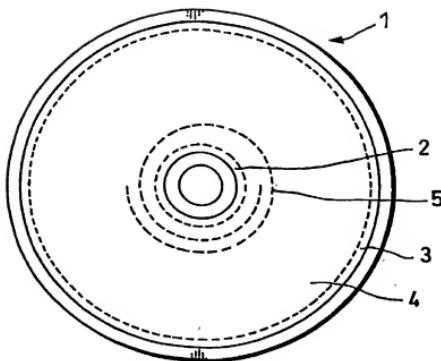


FIG. 2

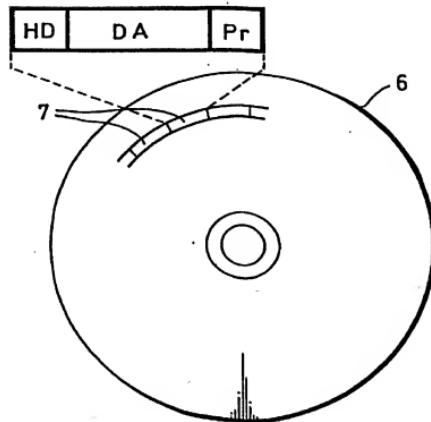


FIG. 3

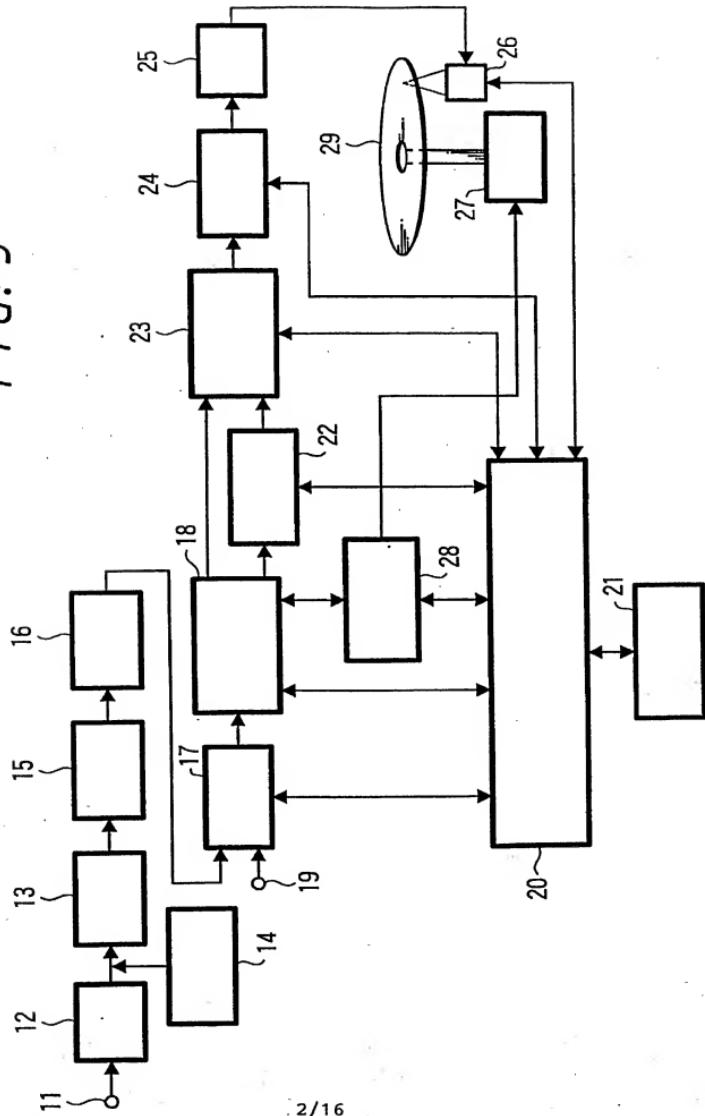


FIG. 4

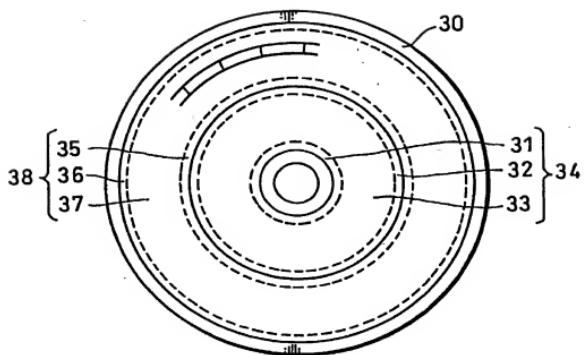


FIG. 5

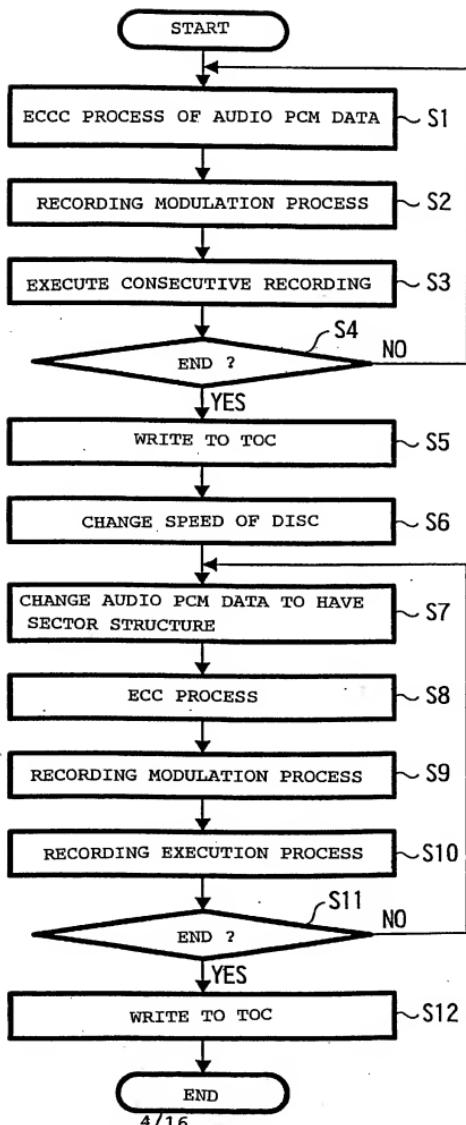
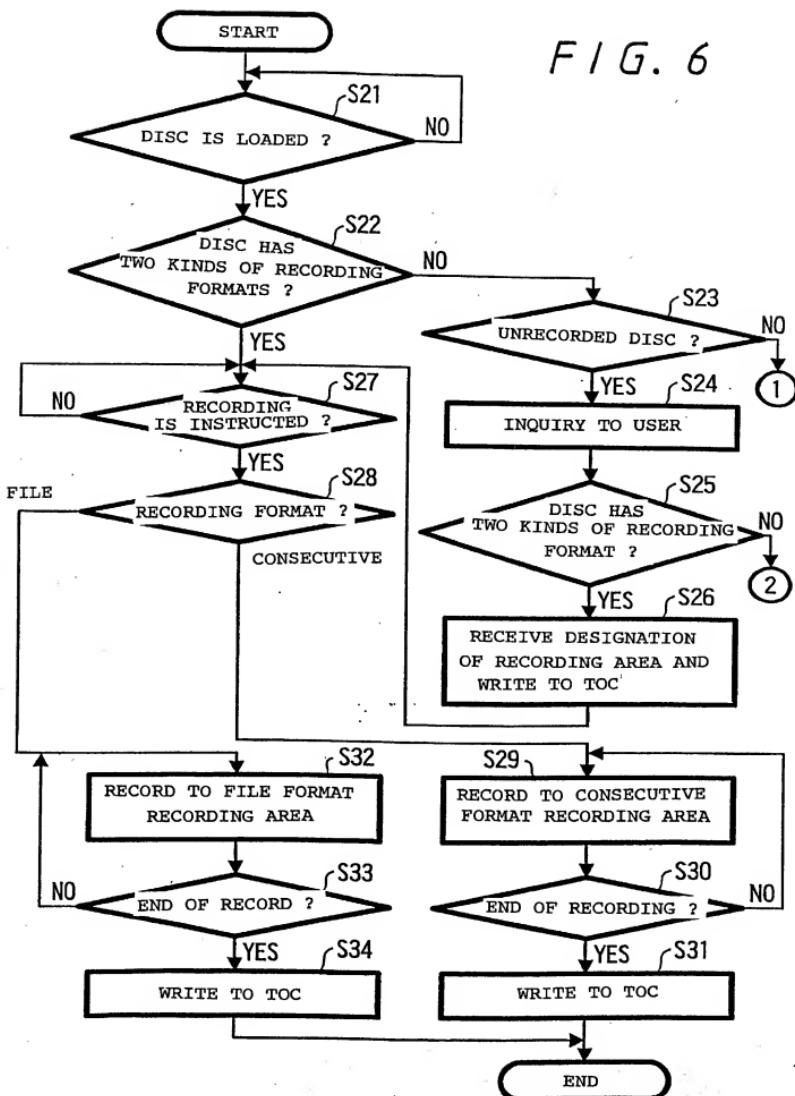


FIG. 6



F I G. 7

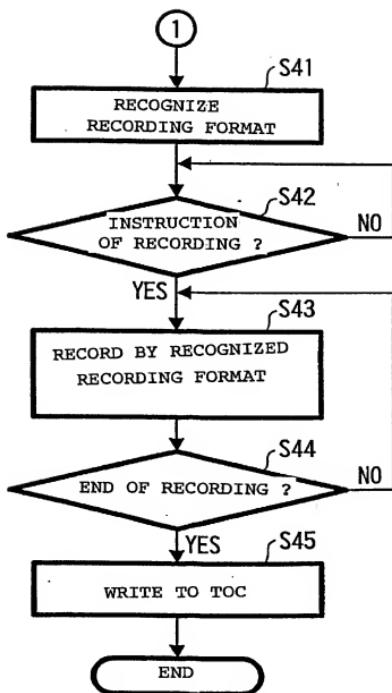


FIG. 8

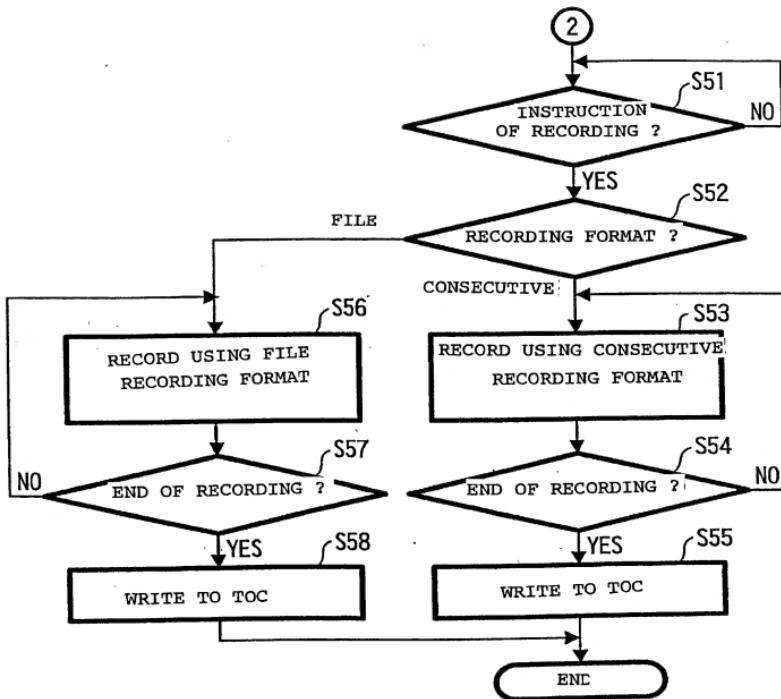


FIG. 9

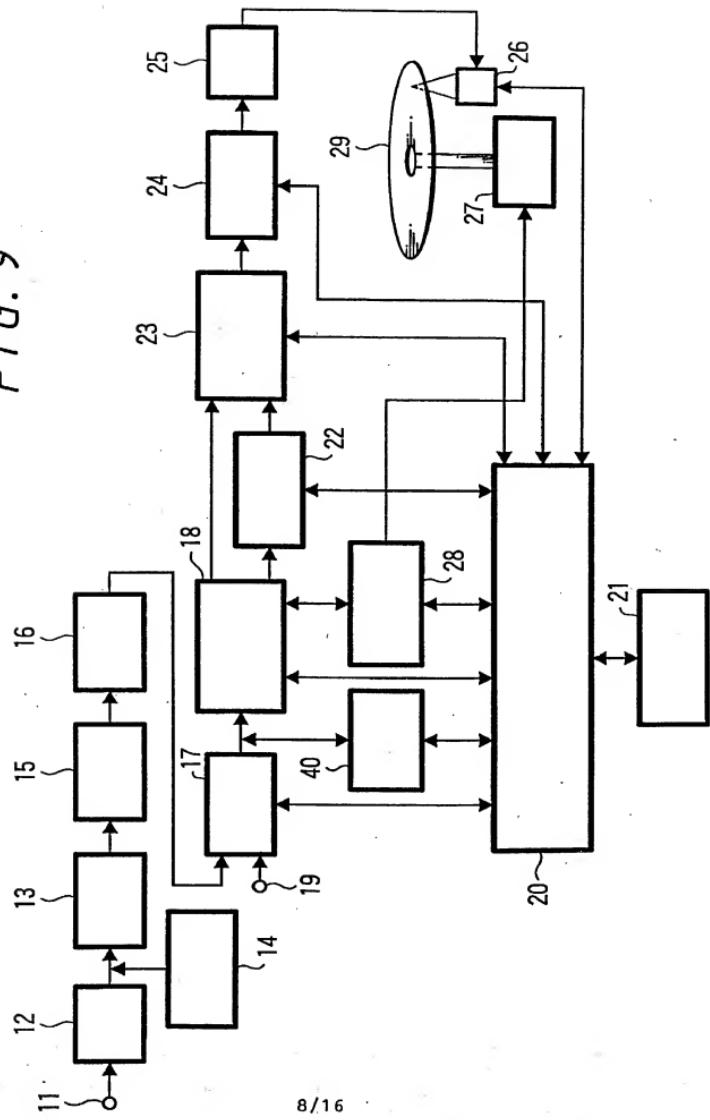


FIG. 10

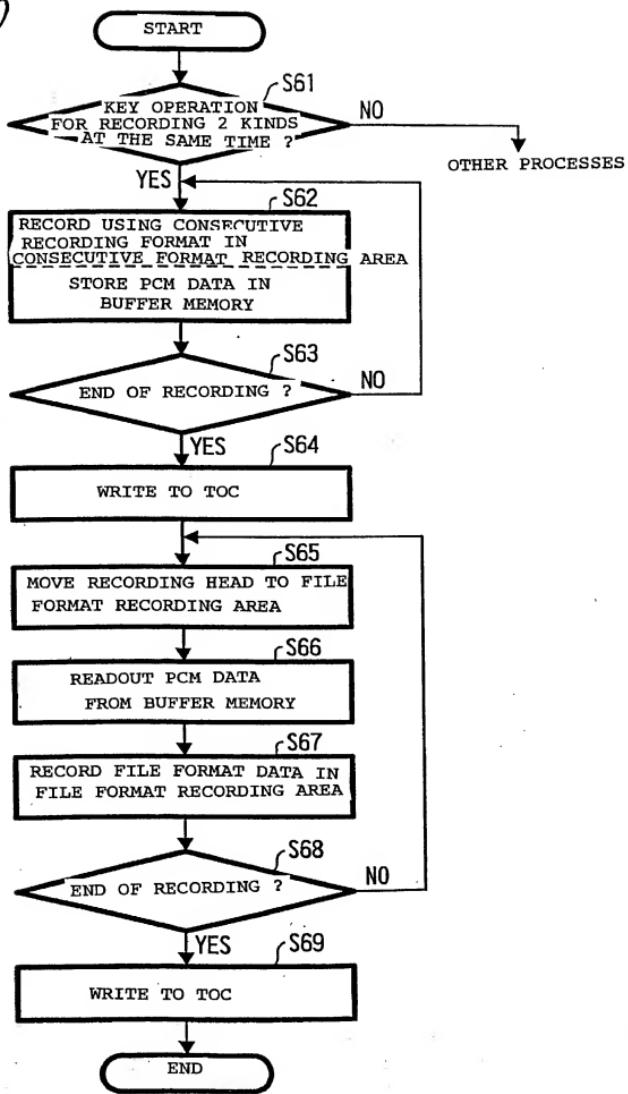


FIG. 11

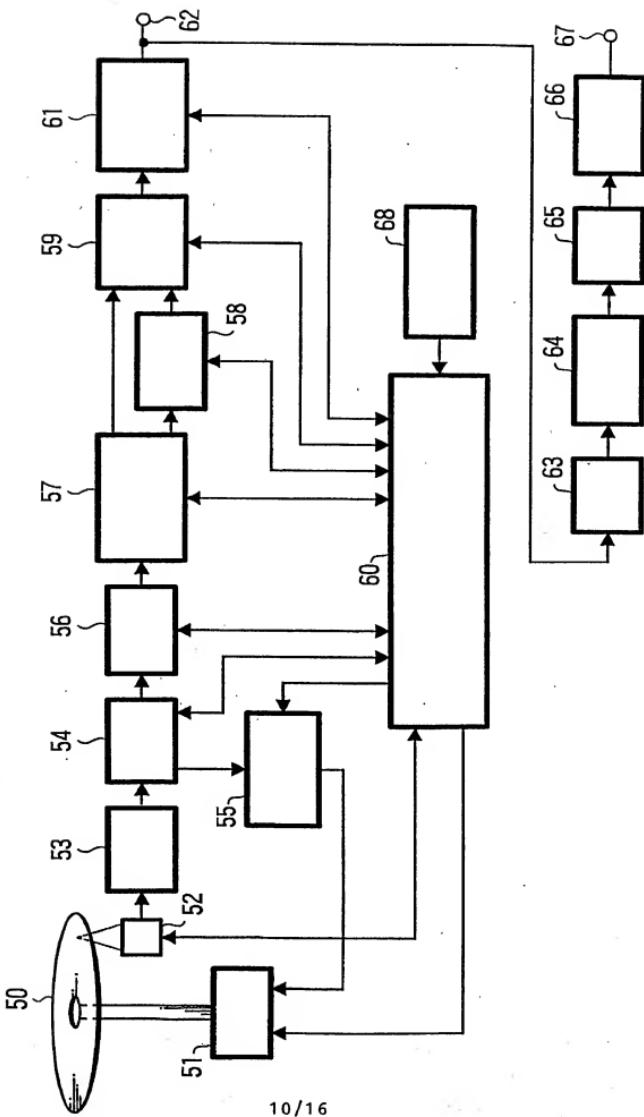


FIG. 12

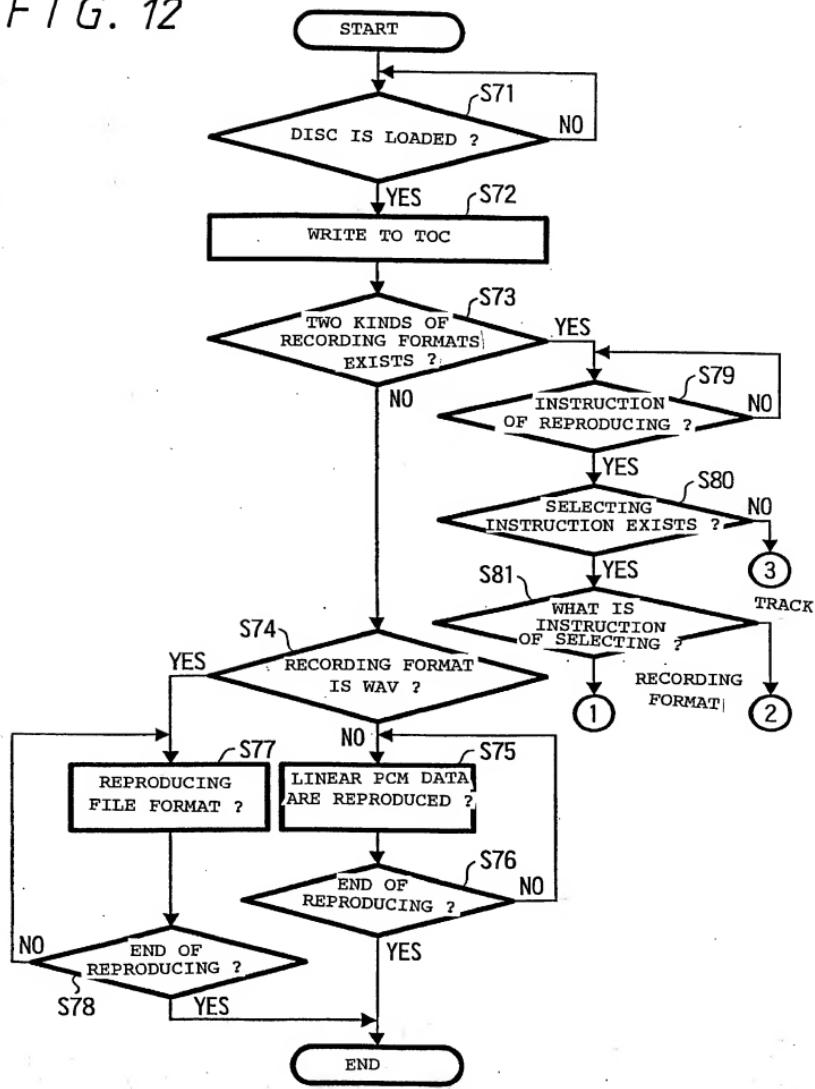


FIG. 13

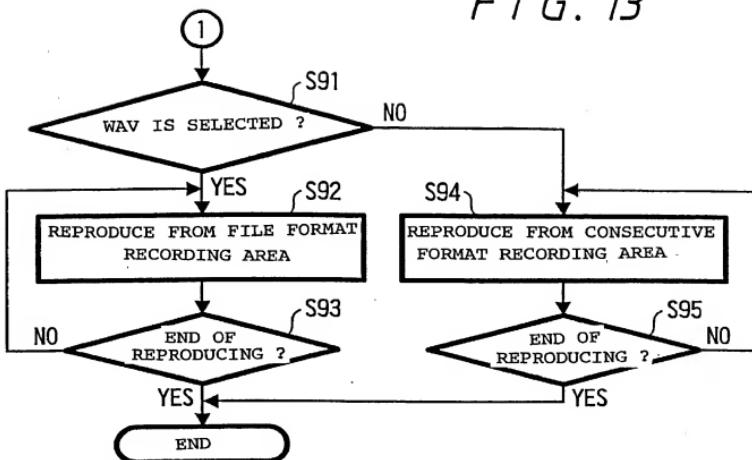


FIG. 14

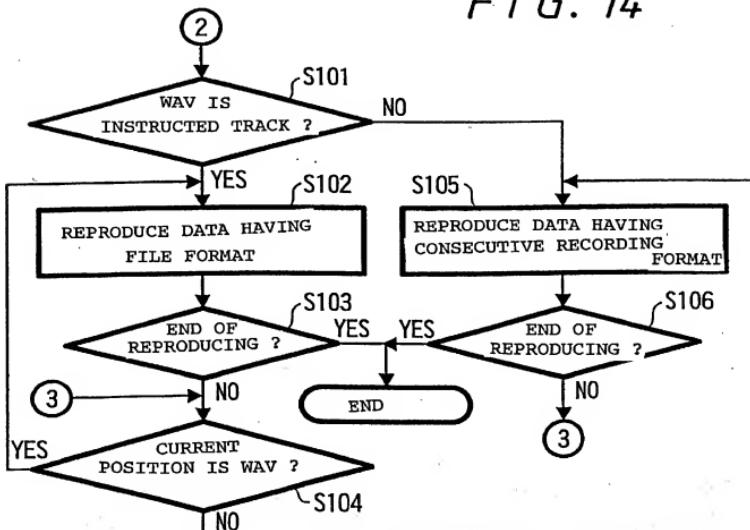


FIG. 15

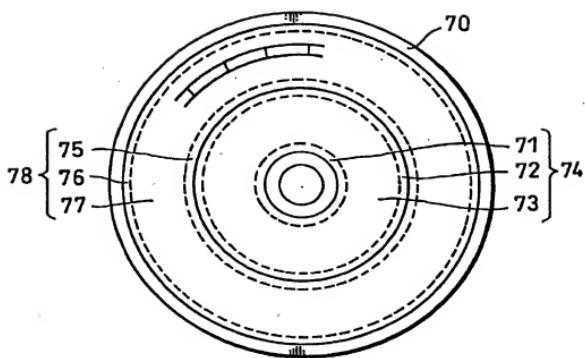


FIG. 16

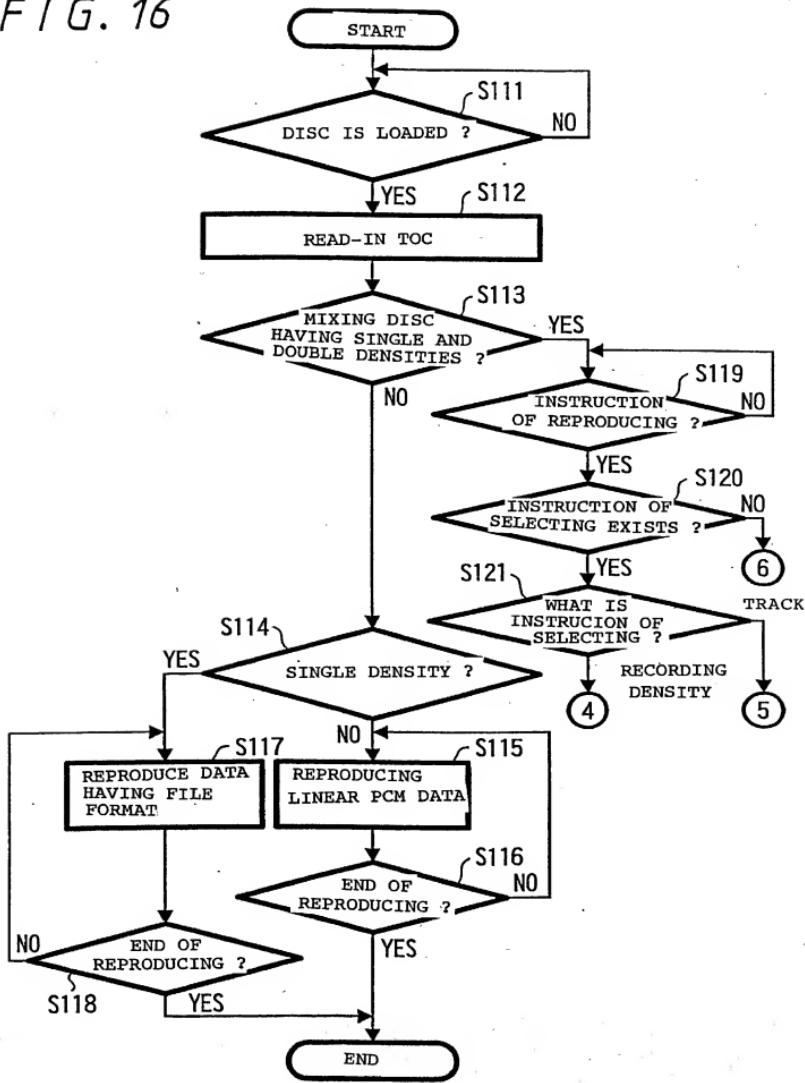


FIG. 17

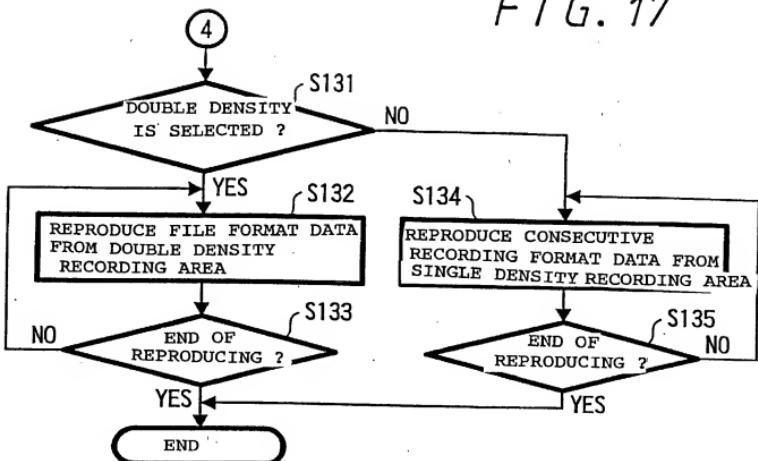
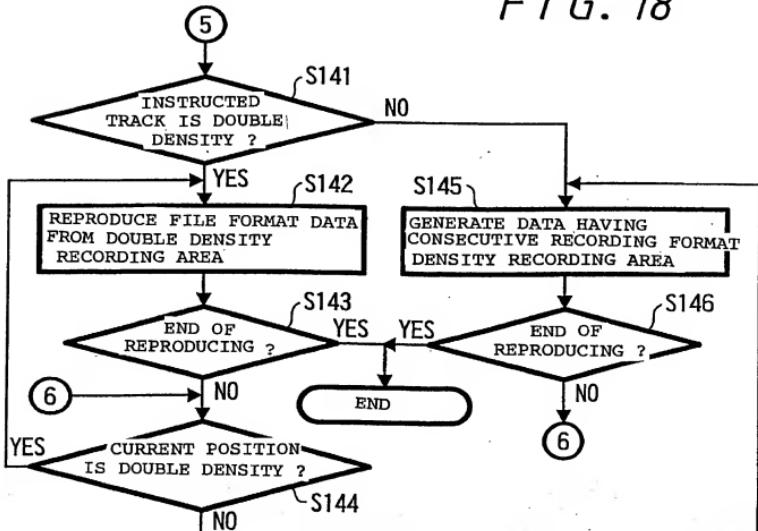


FIG. 18



DESCRIPTION OF REFERENCE NUMERALS

- 11 INPUT TERMINAL
- 12 LINE AMPLIFIER
- 13 LOW PASS FILTER
- 14 DITHER GENERATING CIRCUIT
- 15 SAMPLE AND HOLD CIRCUIT
- 16 A/D CONVERTER
- 17 INPUT SELECTOR
- 18 LINEAR PCM/WA SELECTOR
- 19 DIGITAL SIGNAL INPUT TERMINAL
- 20 SYSTEM CONTROLLER
- 21 KEY INPUT UNIT
- 22 CD-ROM ENCODER
- 23 ECC ENCODER
- 24 RECORD MODULATING CIRCUIT
- 25 RECORDING AMPLIFIER
- 26 RECORDING HEAD
- 27 SPINDLE MOTOR
- 28 SPEED CONTROL CIRCUIT
- 29 OPTICAL DISC